Application Serial No. 10/575,482
Reply to Office Action of September 22, 2008

PATENT
Docket: CU-4757

Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. (currently amended) An encryption processor, ~~comprising: an encryption processor which~~ that connects [[an]] externally ~~connected~~ to a data input and to an output apparatus ~~and an internal data process apparatus~~ and which mediates [[a]] an encrypted communication between the same[[;]], the encryption processor comprising:

an encryption interface that connects externally to the data input, to the output apparatus, and to a password input unit;

[[an]] a password process unit connected to the encryption interface

which receives a password from the password input unit via the encryption interface and

which receives externally inputted data from the data input via the encryption interface and

which encrypts [[an]] the externally inputted data based on a certain encryption algorithm selected from the password and

which detects an abnormal signal and subsequently deletes encrypted data in response to the detected abnormal signal; and

a memory unit which stores a program corresponding to the encryption algorithm and temporarily stores a data generated during an encryption process,
wherein the above encryption processor, password process unit and memory unit are integrated into one independent chip.

2. (currently amended) An encryption processor[[,]] comprising:

a video process module which includes:

a second interface for managing a connection ~~of an externally connected~~ to a video data input unit and to a video output apparatus[[,]];

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a coder for compressing the externally inputted **video** data into a certain format[[,]]**:** and

a decoder for decompressing the compressed video data; and

an encryption module which includes**:**

an encryption a first interface for managing a connection of an externally connected **video data** input **unit, a password input unit,** and **the video** output apparatus[[,]]**:** and

a password process unit

for encrypting [[the]] video data **from the video data input unit via the encryption interface by** using a certain encryption algorithm **selected by using a password received from the password input unit via the encryption interface,** and

for decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm, **and**

for detecting an abnormal signal and subsequently deleting encrypted data in response to detecting the abnormal signal,

wherein the ~~above video process module and~~ encryption **processor is** ~~module are~~ integrated into [[one]] **a single** independent chip.

3. **(currently amended)** An encryption processor[[,]] comprising:

a video process module which includes**:**

a second interface for managing a connection of an externally connected input **unit** and **an** output apparatus,

a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of an externally received video data, **and for adjusting** a coder for compressing the video data into a certain format, and

a decoder for decompressing the compressed video data; and

an encryption module which includes**:**

a ~~first~~ **encryption** interface for managing a connection of ~~an~~ ~~externally connected~~ **the** input **unit,** [[and]] **the** output apparatus, and **a**

password input apparatus;

a password process unit **connected to the encryption interface, the password process unit**

for receiving a password from the video process module via the encryption interface,

for encrypting the video data using a certain encryption algorithm **by using the received password to select the encryption algorithm,**

for detecting an abnormal signal,

for deleting encrypted data in response to detecting the abnormal signal, and

for decoding the encrypted video data using [[a]] **the** certain decoding algorithm corresponding to the encryption algorithm **based on the received password,**

wherein the ~~above video process module and~~ encryption **processor is** ~~module are~~ integrated into one independent chip.

4. **(currently amended)** An encryption processor[[,]] comprising:

a video process module which includes**:**

a second interface for managing a connection of an externally connected input and output apparatus,

a video data generation unit for converting an externally inputted electric signal into a video data,

a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data, a coder for compressing the video data into a certain format, and

a decoder for decompressing the compressed data;

an encryption module which includes**:**

~~a first~~ **an encryption** interface for managing a connection of an externally connected input and output apparatus **of the video process module and for**

<u>connection to a password input apparatus</u>, and

a password process unit <u>for receiving a password form the password</u> <u>input apparatus,</u>

<u>for selecting a certain encryption algorithm based on the</u> <u>received password,</u>

for encrypting the video data using a certain encryption algorithm and decoding the encrypted video data using a certain decoding algorithm corresponding to the encryption algorithm,

<u>for detecting an abnormal signal, and</u>

<u>for deleting encrypted data in response to detecting the</u> <u>abnormal signal,</u>

wherein the above video process module and encryption module are integrated into one independent chip.

5. **(currently amended)** An encryption processor[[,]] comprising:

a video process module which includes<u>:</u>

a second interface for managing a connection of an externally connected input and output apparatus,

a signal compensation unit for removing ~~noises~~ noise from an externally inputted electric signal and <u>for</u> compensating the <u>inputted electric</u> signal <u>in</u> <u>response to the noise</u>,

a video data generation unit for converting the electric signal into a video data,

a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data,

a coder for compressing the video data into a certain format, and

a decoder for decompressing the compressed video data; and

an encryption module which includes<u>:</u>

~~a first~~ <u>an encryption</u> interface for managing [[a]] <u>the</u> connection of [[an]] <u>the</u> externally connected input and output apparatus, <u>and for receiving a</u>

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password from a password input apparatus, and

a password process unit

for receiving a password from a password input apparatus via the encryption interface,

for selecting a certain encryption algorithm based on the received password,

for encrypting the video data using [[a]] **the** certain encryption algorithm and

for decoding the encrypted video data using [[a]] **the** certain decoding algorithm corresponding to the encryption algorithm,

for detecting an abnormal signal, and

for deleting encrypted data in response to detecting the abnormal signal,

wherein the above video process module and encryption module are integrated into one independent chip.

6.   (original) An encryption processor[[,]] comprising:

a video process module which includes:

a second interface for managing a connection of an externally connected input and output apparatus,

a charge coupled device (CCD) for converting an externally inputted light signal into an electric signal,

a signal compensation unit for removing ~~noises~~ **noise** from the electric signal from the CCD and **for** compensating the **noise removed electric** signal,

a video data generation unit for converting the **noise removed** electric signal into a video data,

a video adjusting unit for adjusting a recording environment including a focus, exposure and lighting of the video data by controlling the CCD or the signal compensation unit,

a coder for compressing the video data into a certain format, and

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a decoder for decompressing the compressed video data; and

an encryption module which includes:

~~a first~~ **an encryption** interface for managing a connection of an externally connected input and output apparatus, and

a password process unit

for receiving a password from a password input apparatus via the encryption interface,

for selecting a certain encryption algorithm based on the received password,

for encrypting the video data using [[a]] **the** certain encryption algorithm **based on the received password,** [[and]]

for decoding the encrypted video data using [[a]] **the** certain decoding algorithm corresponding to the encryption algorithm **based on the received password,**

for detecting an abnormal signal, and

for deleting encrypted data in response to detecting the abnormal signal,

wherein the above video process module and encryption module are integrated into one independent chip.

7. **(currently amended)** The apparatus of one among claims 1 through claim 6, further comprising an encryption controller for externally receiving a signal with respect to an operation state of the password process unit and **for** controlling an operation of the password process unit, wherein the encryption controller is adapted to control a size of a password used for an encryption or an encryption operation mode.

8. **(currently amended)** The apparatus of one among claim 1 through claim 6, further comprising a communication module for transferring an internally converted data or a generated data ~~thro0ugh~~ **through** an internally connected communication network.

9. **(currently amended)** The apparatus of one among claim 1 through claim 6, wherein password process unit detects [[an]] the externally received abnormal signal and deletes a certain data for a data encryption corresponding to the abnormal signal receipt and an encrypted video data.

10. **(currently amended)** The apparatus of one among claim 2 through 6, wherein ~~an externally-inputted~~ the password is ~~directly inputted into the encryption module for a data encryption~~ a personal identification number (PIN) password.

11. **(currently amended)** The apparatus of one among claim 2 through claim 6, wherein ~~an externally or internally generated video data is transferred to the password process unit through the second interface and the first interface~~ the encryption algorithm is selected from the group consisting of Electronic Code Book (EBC), Cipher Block Chaining (CBC), Cipher Feed Back (CFB), and Output Feed Back (OFB).

12. **(currently amended)** The apparatus of one among claim 2 through claim 6, wherein said password process unit ~~generates a password used for a data encryption through the first interface in communication with the password input apparatus~~ selects an encryption mode selected from the group consisting of Electronic Code Book (EBC), Cipher Block Chaining (CBC), Cipher Feed Back (CFB), Output Feed Back (OFB) when the password process unit uses a plurality of encryption modes.

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